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Sheet	1	of	1
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Applicati n / C nf. N .	Unknown 101 666667
Filing Date	September 19, 2003
First Named Invent r	Tim Tuan
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	X-1462 US

[illegible][illegible]

2/11/05

¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Complete if Known

Application / Conf. No.	10/666,669 / 8241
Filing Date	September 19, 2003
First Named Inventor	Tim Tuan
Art Unit	2822
Examiner Name	Unknown
Attorney Docket Number	X-1462 US

(use as many sheets as necessary)

Sheet	1	of	2
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[illegible][illegible]

**Examiner
Signature**

Date Considered

2/11/05

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Complete if Known			
		Application / Conf. No.	10/666,669 / 8241		
		Filing Date	September 19, 2003		
		First Named Inventor	Tim Tuan		
		Art Unit	2822		
		Examiner Name	Unknown		
Sheet	2	of	2	Attorney Docket Number	X-1462 US

OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DC		Jae Y. Park, Mark G. Allen; "A Comparison of Micromachined Inductors with Different Magnetic Core Materials"; 1996 Electronic Components and Technology Conference; 1996 IEEE; pp. 375-381.	
DC		Thomas D. Burd et al.; "A Dynamic Voltage Scaled Microprocessor System"; 2000 IEEE; IEEE Journal of Solid-State Circuits, Vol. 35, No. 11, November 2000; pp. 1571-1580.	
DC		Anthony J. Stratakis et al.; "A Low-Voltage CMOS DC-DC Converter for a Portable Battery-Operated System"; 1994 IEEE; pp. 619-626.	
DC		Anthony John Stratakis; "High-Efficiency Low-Voltage DC-DC Conversion for Portable Applications," Chapter 3; "DC-DC Converter Fundamentals"; pp. 42-78, No date	

Examiner Signature	<i>Daniel D. Chay</i>	Date Considered	2/11/05
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